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Ted J. Biggerstaff, Bharat G. Mitbander, Dallas Webster

May 1993 Proceedings of the 15th international conference on Software **Engineering**

Publisher: IEEE Computer Society Press

Full text available: pdf(1.71 MB) Additional Information: full citation, references, citings

Keywords: concept recognition, connectionist, domain, knowledge base, plausible reasoning, reverse engineering, slicing

Program understanding and the concept assignment problem

Ted J. Biggerstaff, Bharat G. Mitbander, Dallas E. Webster May 1994 Communications of the ACM, Volume 37 Issue 5

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Suhyun Kim, Soo-Mook Moon, Jinpyo Park, Kemal Ebcioğlu

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May 2000 Proceedings of the 14th international conference on Supercomputing

Publisher: ACM Press

Full text available: mpdf(1.25 MB)

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Aggressive instruction scheduling leaves behind many renaming copy instructions that cannot be coalesced due to interferences. These copies take resources, and more seriously, they may cause a stall if they are generated for renaming of multi-latency instructions. This paper proposes a code transformation technique based on loop unrolling which makes those copies coalescible. Two unique features of the technique are its method of determining the precise unroll amount based on an idea of

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Biggerstaff, T.J.; Mitbander, B.G.; Webster, D.;

Reverse Engineering, 1993., Proceedings of Working Conference on

21-23 May 1993 Page(s):27 - 43

Digital Object Identifier 10.1109/WCRE.1993.287781

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2. The concept assignment problem in program understanding

Biggerstaff, T.J.; Mitbander, B.G.; Webster, D.;

Software Engineering, 1993, Proceedings., 15th International Conference on

17-21 May 1993 Page(s):482 - 498

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1. Electrical design rule checker for core and block based ASIC designs

Immaneni, V.; Puffer, D.;

ASIC Seminar and Exhibit, 1989. Proceedings., Second Annual IEEE

25-28 Sept. 1989 Page(s):P6/2/1 - P6/2/4

Digital Object Identifier 10.1109/ASIC.1989.123204

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Immaneni, V.; Puffer, D.; Raman, S.;

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17-21 Sept. 1990 Page(s):P13/1.1 - P13/1.6

Digital Object Identifier 10.1109/ASIC.1990.186177

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3. Stimulus generation using the test pattern development language

Manley, M.; Puffer, D.; Perrey, K.;

ASIC Conference and Exhibit, 1991, Proceedings., Fourth Annual IEEE International

23-27 Sept. 1991 Page(s):P15 - 4/1-4

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